BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING
(Approved by AICTE, New Delhi & Affiliated to Guru Gobind Singh Indraprastha University, Delhi)
(An ISO 9001:2008 Certified Institution)
A-4, Paschim Vihar, Main Rohtak Road, New Delhi – 110063

Electronics and Communication Engineering Department

Ref. No.: **BVCOE/ND/ECE/ 213 / 2025-2026** Date: 8/10/2025

NOTICE

All students of **2nd Year** are hereby informed that a **Guest Lecture on "Signals and Systems"** will be conducted as per the following schedule:

Topic: Fundamentals and Applications of Signals and Systems
Speaker: Mr. Aditya Vangala, ACE Engineering Academy
Date: 10 October 2025
Time: 10:00 AM – 12:00 PM
Venue: Online

Mr. Aditya Vangala is a distinguished faculty member at ACE Engineering Academy with rich academic credentials. He holds an M.Tech (Research) from IISc Bangalore .

The session aims to enhance student's understanding of the core concepts of *Signals and Systems* and their practical applications in modern communication and signal processing. **Attendance is mandatory** for all second yearstudents.

Students are required to register in advance for this meeting using the following link: https://aceenggacademy.zoom.us/meeting/register/xohWhZxaRVqbl7_u8pwtfA

After registration, you will receive a confirmation email containing information about joining the meeting.

In case of any query, please contact the undersigned.

Dr. Priyanka Gupta & Dr. Parashuram
(Assistant Professors, ECE Dept.)

8/10/25
Prof. Kirti Gupta
(HOD, ECE)