

BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING
(Approved by AICTE, New Delhi & Affiliated to Guru Gobind Singh Indraprastha University, Delhi)
(An ISO 9001:2015 Certified Institution)
A-4, Paschim Vihar, Main Rohtak Road, New Delhi – 110 063

Ref. No.: BVCOE/EEE/2026-27/Minor Project/01

Date: 16.08.2026

Department of Electrical and Electronics Engineering

Minor Project

All Seventh Semester students of the Department of Electrical & Electronics Engineering are hereby informed about the requirements and submission schedule for the Minor Project for the Academic Year 2026–2027.

Instruction:

1. Each group must consist of 2–3 students only.
2. Students are required to approach EEE faculty members and enroll under their supervision. A maximum of two groups will be allotted to each faculty member.
3. Each group must finalize a suitable Minor Project title in consultation with the respective mentor.
4. The Minor Project group list is attached herewith for reference.

Submission Tasks & Deadlines:

S.No.	Task	Deadline
1	Submit mini project title, group members name and mentor's name.	17-Aug-2026
2	Submit synopsis to respective supervisor	21-Aug-2026



Dr. Bharat Singh
Minor Project, coordinator



Prof. Kusum Tharani
Head of Department, EEE

CC to:

1. Head of Department
2. Departmental Notice Board
3. Website In-charge