



BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING
(APPROVED BY AICTE, AFFILIATED TO GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY)
(An ISO 9001:2015 Certified Institute)
A-4, Paschim Vihar, Main Rohatak Road, New Delhi-110063

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NOTICE

Ref. No: BVCOE/ND/ECE/ 16/2026-2027

Date: 14 August 2026

Industry Talk: From Fundamentals to Industry-Career roadmap in FPGA & Embedded System Design

Date	18 th August 2026
Time and Venue	To be communicated on the morning of 18 August 2026
Participants	All 2nd Year B.Tech. students, Department of ECE

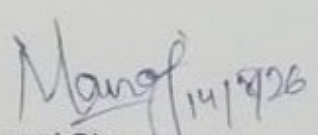
All 2nd Year students are hereby informed that an **Industry Talk on "From Fundamentals to Industry-Career roadmap in FPGA & Embedded System Design"** will be organized by the Department of Electronics & Communication Engineering on 18th August 2026.

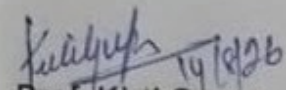
The expert for **M/s Pine Training Academy** would provide practical industry insights into Digital Logic, semiconductor technologies, current industry trends, emerging career opportunities, and the skills expected by the industry.

Attendance is compulsory for all 2nd Year students. Students are required to attend the session punctually and participate actively.

The exact time and venue of the talk will be communicated to the students on the morning of 18th August 2026.

Students are advised to take note of the above and ensure their presence.


Dr. Manoj Sharma
Associate Professor
Dept. ECE


Prof. Kirti Gupta
Vice Principal (Academics) & HoD
Dept. ECE