



BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi & Affiliated to Guru Gobind Singh Indraprastha University, Delhi)

(An ISO 9001:2015 Certified Institution)

A-4, Paschim Vihar, Main Rohtak Road, New Delhi - 110 063

Department of Electronics and Communication Engineering

Ref No: BVCOE/ND/ECE/35/23-24

NOTICE

Date: 17th Oct 2023

Kind attention: All students ECE dept [3rd year]

Subject: Regarding-Training on Digital Circuit Layout design and IC-Projectathon.v.2.0 - 2022:

- **Compulsory for 3rd year**
- **Class adjustment**
- **Project submissions of Projectathon-2023 to be included in Microelectronics lab "Content Beyond Syllabus (CBS)"**

Dept of ECE, is organizing the IC-Projectathon.v.2.0 -2023 (project-based competition in IC layout design). For the same it is informed that:

1. All 3rd year students need to participate compulsorily in this 2-day event.
2. As per timetable on the 2 days the attendance and performance would be monitored by their respective faculty members.
3. The project submissions would be taken as 'Content Beyond Syllabus (CBS)' for Microelectronics lab work.
4. The schedule to compensate for regular classes on both these days, will be notified later.
5. The first day technical lectures and hands-on session by industry experts is on digital circuit layout design that would also cover part of topic in 5th semester subject- 'Microelectronics theory and lab work'. Students would be expected to make their notes in the hands-on training that would be useful during the IC-Projectathon.
6. After the training 'Projectathon-2023' would start on 2nd day. The IC projectathon-2023 would be 'Open Book/Open Internet Design Contest'. Student's groups (max 5 in a group) would work on projects and would implement and present the design.
7. The program of the 2-day event is as under:

(1/2)

S.No.	Date	Time	Event
1	19 th Oct 2023	10 am -10.30 am	Opportunities in VLSI Industries
2		10.30 am- 1.00 pm	Layout design hands-on session 1
3		2 pm - 4 pm	Layout design hands-on session 2
4	20 th Oct 2023	10 am - 3.30 pm	IC Layout design projectathon
5		3.30 pm - 5pm	Result and conclusion
Venue: Computer lab 4th floor A404			

1st winner will get a prize of 2.5K, 2nd winner will get the prize of 1.5K and 3rd winner will get prize of 1K.

Students are requested to register themselves for the same using the link provided. Please join the WhatsApp-group. Students can interact and resolve any query on this group.

Registration link:

<https://forms.gle/bJtJJSUEarNedYH39>

WhatsApp Group link

<https://chat.whatsapp.com/Exu4IRNpqqM8zYGecf00YQ>

Manoj
Dr. Manoj Sharma

Projectathon-2023 Coordinator Assoc Prof (ECE)

Kirti Gupta
Prof. Kirti Gupta
HoD (ECE)

(2/2)