

## BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi & Affillated to Guru Gobind Singh Indraprastha University, Delhi)

(An ISO 9001, 2015 Certified Institution)

A-4, Paschim Vihar, Main Rohtak Road, New Delhi – 110 063

## Department of Electronics and Communication Engineering

Ref. No.: BVCOE/ND/ECE/52/2024-2025

Date: 10/08/2024

## **NOTICE**

All the Final year ECE students are hereby informed that they all have to form a group of four students for Minor Project. The list of area(s) of interest of faculty members is attached with this notice. Students can meet the faculty members as per their interest area for the supervision. Kindly note that only four students will be allowed in the group. Kindly do the submissions of group to the respective class advisor after the approval of the mentor latest by 16<sup>th</sup> August24. After the due date, mentor of the Minor project will be assigned by the class advisors.

For any further query, please contact your respective class advisors.

## Following to be submitted to the class advisors

Mentor name (After the Approval)

Name of the students in a group

Title of the Project

Final Year Class Advisors:

Dr. S B Kumar, Dr. Shweta Gupta (ECE-CSE2)

Mr. Heilder Kumar, Mr. Bhawanand Jha (ECE-CSE1)

Ms. Shikha, Mr. Parashuram Patel (ECE-CORE, AI/ML)

Copy to:

- 1. Principal
- 2. HOD(ECE)
- 3. Class Advisors (4th Year ECE)
- 4. Students Notice Board
- 5. Website

Prot. Rirti Gupta (HOD ECE)



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Or. Kirti Gupta PE			OFESSOR,HOD &VICE	Digital circuit design
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			NCIPAL-ACADEMIC	Digital System Design, VLSI
			SOCIATE PROFESSOR	Microprocessor and Microcontroller, Embedded
Ms. Neera Agarwal		AS	SOCIATE PROFESSOR	
		1		system
Dr. Surjeet Balhara Dr. Manoj Sharma		AS	SOCIATE PROFESSOR	Wireless communication, Data Communication a
				Networks (Ad hoc Networks), IoT, & MLI
		IAS	SSOCIATE PROFESSOR	VLSI System Design, VHDL/Verilog/ Synthesies.
DI. N	rianoj Sharma	1		FPGA/ Transistor level, Layer level, IOT
ID:	Dr. Yugnanda		SSOCIATE PROFESSOR	DWDM Design and EON, Optical Communication
Dr. Tugnanda		10		Systems
Dr.	Dr. Saji Antony		ASSOCIATE PROFESSOR	Analog Circuits Design, Low power Sensor Nodes
	Dr. Shashi Kumar		ASSOCIATE PROFESSOR	Signals & Systems, Microstrip antenna, Microwave
Dr. Shashi Kumar		\'	ASSOCIATE PROFESSOR	Engineering, Wireless Communication,
- 1				Communication System
Linear Land	the state of the s	- 1-346	ASSOCIATE PROFESSOR	Analog and Digital circuits, VHDL, Memories
Dr. Monica Gupta Dr. Suman Yadav			ASSOCIATE PROFESSOR	Digital Signal Processing
and the same of th			ASSISTANT PROFESSOR	VLSI design, Embedded Systems, VHDL
Mr. Gauray Mitra Dr. Ruchi Dr Monica Bhutani			ASSOCIATE PROFESSOR	Embedded Systems, OPOC, Signal Processing,
		1	7.00001112111012000	Machine Learning
		ni	ASSOCIATE PROFESSOR	Visiable Light Communication, Optical Wireless
				Communication, Computer Networks with Random
				Processes (Markovian Processes), LiFi Technology
14	Dr. Apoorva		ASSISTANT PROFESSOR	Signal processing, Optimization, Digital Filters,
			45.5	Image processing
15 Mr. Bhawanand Jh		ha	ASSISTANT PROFESSOR	Digital VLSI Design, Embedded System
16 Dr. Annu Dabas			ASSISTANT PROFESSOR	Analog circuits design, VLSI Design
17 Dr. Rajiv Nehra			ASSISTANT PROFESSOR	Microwave antennas, Microwave devices
18	Dr. Suman Yadav	V	ASSISTANT PROFESSOR	Digital Signal Processing, Image processing,
19 Dr. Rubeena Voh			ASSISTANT PROFESSOR	Machine Learning Digital Image Processing, Remote Sensing, Digital
-		ıra	ASSISTANT PROFESSOR	Signal Processing and its Application
19	Di. Rubecha voi			
			ASSISTANT PROFESSOR	
20	Mr. Sourabh Rar		ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas  Antenna Design, Optimization using Al/ML
20	Mr. Sourabh Rar Mr. Jitender Kur		ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas
20	Mr. Sourabh Rar		ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using AI/ML
20 21 22	Mr. Sourabh Rar Mr. Jitender Kur Mr. Avinash		ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using Al/ML RF & Microwave
20 21 22	Mr. Sourabh Rar Mr. Jitender Kur Mr. Avinash	nar	ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using Al/ML RF & Microwave Photonic Devices, VLSI Design, Machine
20 21 22 23	Mr. Sourabh Rar Mr. Jitender Kur Mr. Avinash Dr Jyoti	nar	ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using Al/ML RF & Microwave Photonic Devices, VLSI Design, Machine Learning Analog Circuit Design, Microelectronics Analog Integrated Circuits
20 21 22 23 24	Mr. Sourabh Rar Mr. Jitender Kur Mr. Avinash Dr Jyoti	nar	ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using Al/ML RF & Microwave Photonic Devices, VLSI Design, Machine Learning Analog Circuit Design, Microelectronics
20 21 22 23 24 25	Mr. Sourabh Rar Mr. Jitender Kur Mr. Avinash Dr Jyoti Dr. Priyanka Gu Dr. Shweta Gup	nar ipta ta	ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR ASSISTANT PROFESSOR	Microwave and Antennas Antenna Design, Optimization using Al/ML RF & Microwave Photonic Devices, VLSI Design, Machine Learning Analog Circuit Design, Microelectronics Analog Integrated Circuits

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